

FEATURES

- 60A switching capability
- 1 Form A , 1 Form B and 1 Form C configurations
- 2.5KV Dielectric strength between coil and contacts

**CONTACT DATA**

Contact arrangement	1A/1B/1C
Contact resistance	100m Ω (at 1A 6VDC)
Contact rating	1C: 60A/250VAC 60A/30VDC:
Contact material	AgCdO/AgSnO ₂ /AgNi

CHARACTERISTICS

Insulation resistance	500M Ω 500VDC
Dielectric strength	Between Coil & contact: 2500VAC,1min Between open contact: 1200VAC,1min
Operate time	25 ms
Release time	25 ms
Vibration resistance	10-55Hz, DA 1mm
Shock resistance	10G(Half-sine shock pulse 11ms)
Humidity	45% ~ 75%RH
Ambient Temp.	-25°C ~ +55°C
Electrical life	1 \times 10 ⁵ ops
Mechanical life	1 \times 10 ⁷ ops

COIL DATA

Rated voltage (VDC)	Pick-up Voltage VDC (Max.)	Drop-out Voltage VDC (Min.)	Rated Current (A \pm 10%)	Coil resistance (Ω \pm 10%)	Power (W)
6	4.5	0.6	0.33	18	2
12	9	1.2	0.17	72	2
24	18	2.4	0.083	288	2
48	36	4.8	0.042	150	2
60	45	6	0.033	2800	2
110	82.5	11	0.018	9650	2

Notes: 1) All values unspecified are at room temperature.

2) Only typical loads are listed above. Other load specifications can be available upon request.

3) For sealed type, the vent-hole cover should be excised.

COIL DATA

Rated voltage (VAC)	Pick-up Voltage VAC (Max.)	Drop-out Voltage VAC (Min.)	Rated Current (A ± 10%)	Coil resistance (Ω ± 10%)	Power (VA)
6	4.8	1.8	1.116	14.4	8.6
12	9.6	3.6	0.559	57	8.6
24	19.2	7.2	0.279	250	8.6
48	38.4	14.4	0.14	921	8.6
110	88	36	0.061	5000	8.6
220	176	72	0.003	19600	8.6

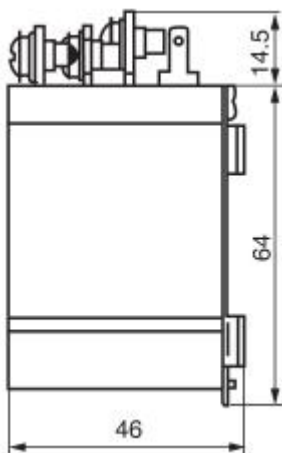
ORDERING INFORMATION

JRZ - **1C** **12VDC**

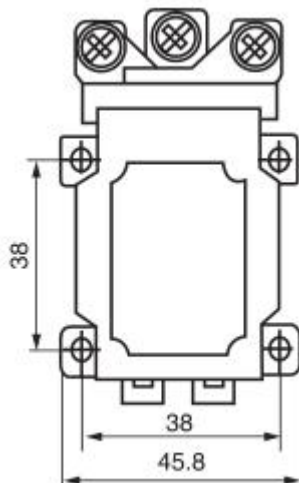
Type	Contact arrangement	Coil voltage
JRZ JQX-60F	1A/1B/1C	5VDC/6VDC/9VDC/12VDC/24VDC/30VDC/36VDC/48VDC/60VDC/72VDC/110VDC/220VDC 12VAC/24VAC/110VAC/220VAC/380VAC

OUTLINE DIMENSIONS, WIRING DIAGRAM AND LAYOUT (Unit: mm)

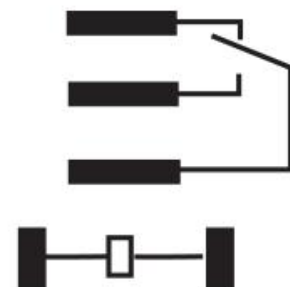
Outline Dimensions



Layout (Bottom view)



Wiring Diagram (Bottom View)



Remark:1) In case of no tolerance shown in outline dimension: outline dimension ≤ 1mm, tolerance should be ± 0.2mm; outline dimension > 1mm and ≤ 5mm, Tolerance should be ± 0.3mm; outline dimension > 5mm, tolerance should be ± 0.4mm.

2) The tolerance without indicating for PCB layout is always ± 0.1mm